UNIVERSITY OF KERALA

TIMETABLE FOR FOURTH SEMESTER REGULAR/SUPPLEMENTARY B.TECH DEGREE PRACTICAL EXAMINATION FOR ELECTRONICS AND COMMUNICATION ENGINEERING (412), COMPUTER SCIENCE AND ENGINEERING(415) & INFORMATION TECHNOLOGY (416), 2020 SCHEME, JANUARY 2024 UNIVERSITY COLLEGE OF ENGINEERING KARIAVATTOM (REGULAR-2021 ADMISSION & SUPPLEMENTARY-2020 ADMISSION CANDIDATES)

Course	Subject	Date	Centre
Computer Science and Engineering (415)	CSL 202 Digital Lab	14.3.2024 FN Thursday 15.3.2024 FN Friday 18.3.2024 FN Monday 19.3.2024 FN Tuesday	University College of Engineering, Kariavattom
	CSL 204:Operating Systems Lab	19.3.2024 FN Tuesday	
Information Technology (416)	ITL 202: Object Oriented Techniques Lab	15.3.2024 FN Friday	
		18.3.2024 FN Monday 19.3.2024 FN Tuesday	
Electronics And Communication Engineering (412)	ECL 204: Microcontroller Lab	18.3.2024 FN Monday 19.3.2024 FN Tuesday	
	ECL 202: Analog Circuits and Simulation Lab	21.3.2024 FN Thursday 22.3.2024 FN Friday	

Thiruvananthapuram Dated:6.3.2024

Sd/-Controller of Examinations